

IBM DOCKET: FIS920000168US1

PATENT

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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

INVENTOR: John W. Cole ) EXAMINER:  
SERIAL NO.: ) ART UNIT:  
FILING DATE: ) DATE: December 18, 2000  
FOR: Ferris-Wheel Queue )

JC841 U.S. PTO  
09/756939  
01/09/01

**INFORMATION DISCLOSURE STATEMENT**

Assistant Commissioner for Patents  
Washington, D.C. 20231

Dear Sir:

In accordance with 37 CFR 1.56, 1.97 and 1.98, the following items are made of record to assist the Patent & Trademark Office in its examination of this application and is, in the opinion of the attorney designated below for applicant(s), information relevant to the closest prior art of which that person is aware. The filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made or that no other art than that identified exists.


<b><u>PATENT NO.</u></b>	<b><u>INVENTOR</u></b>	<b><u>ISSUE DATE</u></b>
5,910,178	Moh et al.	06/08/99
5,878,228	Miller et al.	03/02/99
5,533,020,	Byrn et al	07/02/96
5,884,890	Delp et al.	12/01/98
5,916,307	Piskiel et al.	06/29/99
5,982,776	Manning et al.	11/09/99
5,276,896	Rimmer et al.	01/04/94

**OTHER ART**

IBM Technical Disclosure Bulletin "Queueing Mechanism for Bi-Processor Communication" Vol. 40, No. 05, May 1997, pp. 169-171.

A copy of each of the foregoing items and Form PTO 1449 are enclosed herewith.

Respectfully submitted,

By:   
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CERTIFICATE OF MAILING UNDER 37 CFR 1.10

I hereby certify that, on the date shown below, this correspondence is being deposited with the United States Postal Service in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231, as "Express Mail Post Office to Addressee"  
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<b>FORM PTO-1449</b> (Modified) <b>U.S. DEPARTMENT OF COMMERCE</b> <b>PATENT AND TRADEMARK OFFICE</b>  <b>INFORMATION DISCLOSURE</b> <b>STATEMENT BY APPLICANT</b>  (Use several sheets if necessary) (37 CFR 1.98(b))	<b>ATTY. DOCKET NO.:</b> FIS920000168US1	<b>SERIAL NO.:</b> 09/756,939
	<b>APPLICANT:</b> John W. Cole	
	<b>FILING DATE:</b> 01/09/2001	<b>GROUP:</b> 2/72

U.S. PTO  
 09/756,939  
 01/09/01

## REFERENCE DESIGNATION

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL	PATENT NUMBER	ISSUE DATE	PATENTEE	CLASS	SUB- CLASS	DATE IF APPROPRIATE
K	5,910,178	06/08/99	Moh et al.			
K	5,878,228	03/02/99	Miller et al.			
B	5,533,020,	07/02/96	Byrn et al			
K	5,884,890	12/01/98	Delp et al.			
K	5,916,307	06/29/99	Piskiel et al.			
A	5,982,776	11/09/99	Manning et al.			
K	5,276,896	01/04/94	Rimmer et al.			

## FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY OR PATENT OFFICE	CLASS	SUB- CLASS	TRANSLATION	
						YES	NO

## OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

	IBM Technical Disclosure Bulletin "Queueing Mechanism for Bi-Processor Communication" Vol. 40, No. 05, May 1997, pp. 169-171.
<b>EXAMINER</b>	<b>DATE CONSIDERED</b>
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	